## VOLTAGE DETECTING SYSTEMS

01.10.2019



This VDS is based on the sharing of voltage between capacitor C1 (high voltage) and capacitor C2 (low voltage); the signal at C2 terminals is transformed in an optical signal, which separately points out voltage and phase of the line involved.

Thanks to this new system the signals of voltage get to the operator through a galvanic (optical) insulation, which never transfers voltage, even in case of failure of capacitor C1.

The IEC Standard 61243-5 1997-06 is applicable to our Voltage indicator. At page 11 point 1.2, the standard concerns VDS "based on fundamentally different principles (for examples optical systems, " ...); they "should meet the requirements of this standard where applicable."

The very small size allows to reduce space in your panel and in the meanwhile maximizes the ratio quality/cost.

### HVD3/M/DIP/

- Optical Integrated VDS Voltage detecting system in accordance with IEC 61243-5 where applicable
- The device supplies continuously:
- A synchronous optical signal which can be used either for local voltage indication or as phase signal to be analysed by phase comparator (PD)
- Selectable sensitivity
- LED life time guaranteed min. 30 years
- Surge arresters does not applied because only optical signals are available on the front of panel

#### **Technical features**

High voltage:	3 - 300 pF requested < 1mW acd/20mA 275KV 650KV 6/026630
IP degree protection :	IP64

\*Versions with customized features can be provided.

#### **Material**

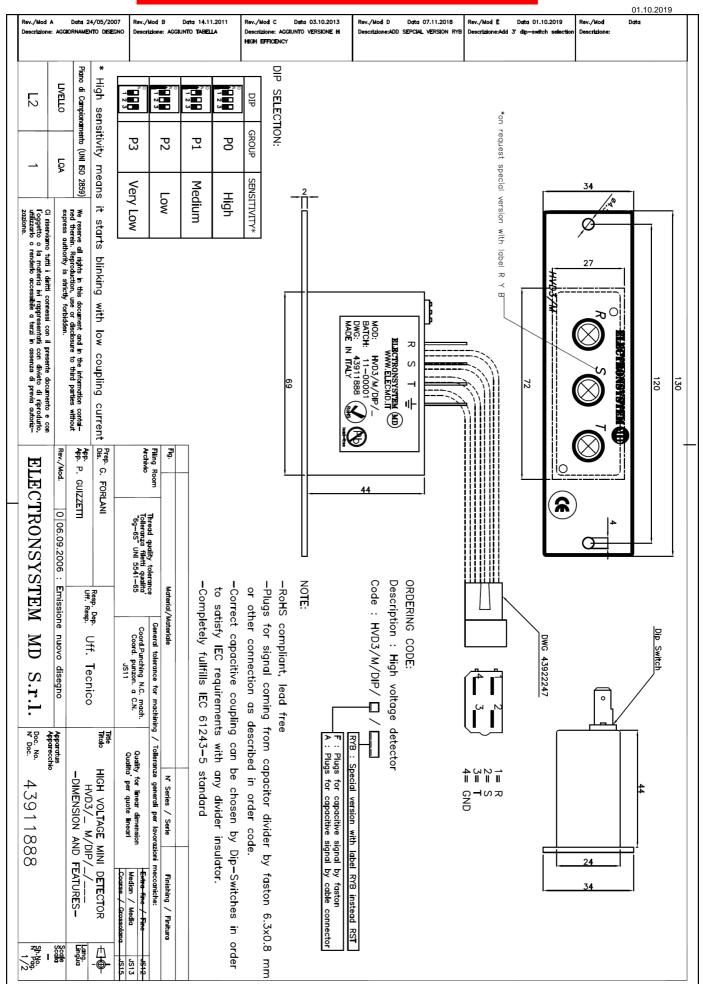
Box :..... plastic housing filled by Polyurethan resin (2-component)

Connection input :...AMP waterproof connectors(\*) or.....faston 6.3X0.8 (IP30)

Cable: ......Reiter Lappkabel 0015703 approved VDE(NYSLYCYÖ-J) SEV(CH-NO5VC4V5-F) UL(AWM Style 2587) CSA(AWM I A/B II A/B) (\*)

(\*) on request

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01.10.2019 Rev./Mod A Data 24/05/2007 Rev./Mod B Data 14.11.2011 Rev./Mod C Data 03.10.2013 Rev./Mod D Data 07.11.2018 Rev./Mod E Data 01.10.2019 Rev./Mod ne: AGGIUNTO TABELLA HIGH EFFICENCY Piano di Campionamento LIVELLO Capacity [pF] Capacity [pF] L2 50,0 45,0 40,0 35,0 30,0 25,0 20,0 15,0 5,0 60,0 55,0 55,0 55,0 45,0 40,0 35,0 35,0 25,0 25,0 15,0 15,0 2 2 (UNI ISO 2859) 4 PO \_ 6 6 We reserve all rights in this document and in the information ned therein. Reproduction, use or disclosure to third parties express authority is strictly forbidden. Ci riserviamo tutti i diritti connessi con il presente documento e con l'aggetto o la materia ivi rappresentati con divieto di riprodurlo, utilizzario o renderio accessibile a terzi in assenza di previa autoriz-8 8 COUPLING TABLE P1 COUPLING TABLE PO 10 10 \_  $\Rightarrow$ 12 13 14 Voltage [kV] 12 13 14 Voltage [kV] 15 15 16 16 17 17 18 18 M AXIMUM CAPACITANCE 19 contai-19 20 20 21 21 <del>≱</del>₽ ₽.₽ Filing Room Archivio 9 Dis. G. FORLANI Rev./Mod. 22 22 ELECTRONSYSTEM MD 23 23 GUIZZETTI 24 24 0 05.09.2006 : Emissione nuovo disegno Thread quality tolerance Tolleranza filetti qualita' "6g—6S" UNI 5541—65 Capacity [pF] Capacity [pF] 200,0 400,0 500,0 175,0 100,0 150,0 200,0 250,0 300,0 350,0 450,0 125,0 150,0 100,0 50,0 50,0 25,0 75,0 0,0 0,0 2 3 4 5 3 4 Resp. Dep. Uff. Resp. Material/Materiale 5 6 7 6 Coord.Punching N.C. r Coord. punzon. a C JS11 General tolerance for machining 7 Uff. Tecnico 8 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 S.r.l.**COUPLING TABLE P2 COUPLING TABLE P3** C.N. Doc. No. Voltage [kV] Voltage [kV] / Tolleranze generali per lavorazioni Quality for linear dimension Qualita' per quote lineari HIGH VOLTAGE MINI DETECTOR

HVD3/\_ M/DIP/\_/\_\_\_

-COUPLING TABLES-N° Series / Serie 43911888 M AXIM UM CAFACITANCE Extra fine / Fine
Median / Media
Coarse / Grossola meccaniche: Finishing / Finitura <u>ф</u> ያት-ዜነ። 2/2 I Scale JS13